

NCP2860

300 mA Very Low Noise, Low Dropout Linear Regulator

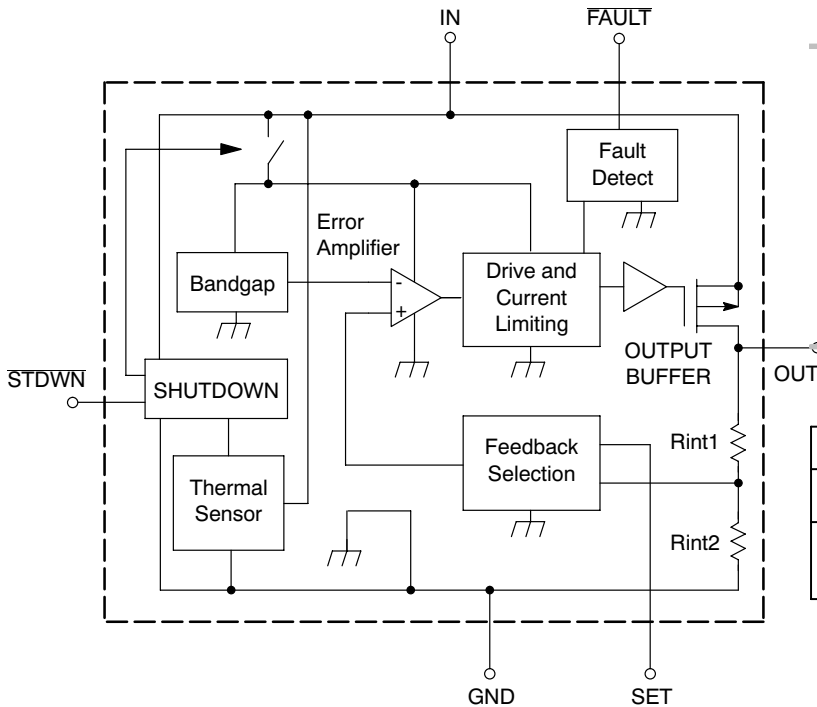
The NCP2860 is a low noise, low dropout linear regulator that is offered with an output voltage of 2.77 V. It supplies 300 mA from 3.0 V to 6.0 V input. If wished, the “SET” pin enables to adjust the output voltage level that then depends on the voltage applied to this pin. The excellent performances that the NCP2860 features in terms of transient responses, PSRR and noise, make it an ideal solution for audio applications (e.g., audio amplifier drivers).

Features

- High Output Current (300 mA Max)
- Low Output Voltage Noise: 60 μ Vrms
- Low Dropout (150 mV @ $I_{out} = 300$ mA)
- Thermal Overload and Short Circuit Protections
- Very Low Consumption in Shutdown Mode (10 nA)
- High Power Supply Rejection Ratio (60 dB @ 1.0 kHz)
- **FAULT** Indicator
- Programmable Output Voltage
- Soft-Start
- Pb-Free Package is Available

Typical Applications

- Cellular Phone
- Handheld Instruments



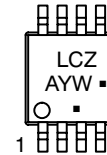
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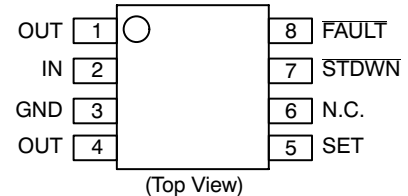
**Micro8™
DM SUFFIX
CASE 846A**

MARKING DIAGRAM



LCZ = Specific Device Code
 A = Assembly Location
 Y = Year
 W = Work Week
 ■ = Pb-Free Package
 (Note: Microdot may be in either location)

PIN CONNECTIONS



ORDERING INFORMATION

Device	Package	Shipping†
NCP2860DM277R2	Micro8	4000/Tape & Reel
NCP2860DM277R2G	Micro8 (Pb-Free)	4000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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PIN DESCRIPTION

Pin	Name	Description
1, 4	OUT	"OUT" is the regulator output. A low ESR, bypass capacitor should be connected for stable operation.
2	IN	"IN" is the supply input that is connected to the power source (up to 6.0 V). Bypass with a 2.2 μ F capacitor.
3	GND	Ground
5	SET	Ground the "SET" pin to set the output voltage to 2.77 V. Refer to the "output voltage setting" paragraph if you need to program another value.
6	N.C.	This pin is non-connected.
7	STDWN	If the "STDWN" pin is low, the circuit enters the shutdown mode.
8	FAULT	The "FAULT" terminal is a high impedance, open drain output. If the circuit is out of regulation, the voltage pin goes low. Otherwise (normal operation or shutdown mode), this pin is high impedance. Connect the pin to ground, if unused.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Input Voltage, Shutdown Pin, Voltage Range (Note 1)	V_{inmax}	-0.3, +6.0	V
Thermal Resistance (Note 2)	-	230	$^{\circ}$ C/W
Maximum Junction Temperature	T_{Jmax}	150	$^{\circ}$ C
Storage Temperature Range	T_{Smax}	- 65 to +150	$^{\circ}$ C
Lead Temperature (Soldering, 10s)	T_{Lmax}	300	$^{\circ}$ C
ESD Capability Human Body Model	-	2.0	kV
Machine Model	-	200	V
Latchup Capability @ 85 $^{\circ}$ C	-	+/-100	mA

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. The recommended input voltage range for NCP2860 proper operation is 2.7 V to 6.0 V.
2. Circuit being mounted on a board that has no metal oxide traces attached to the leads. The addition of plated copper can lower the thermal resistance.

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TYPICAL ELECTRICAL CHARACTERISTICS (Note 3) ($V_{in} = 3.6\text{ V}$, SET = GND, T_A from -25°C to $+85^\circ\text{C}$, unless otherwise noted.)

Characteristic (2.7 V Option)	Symbol	Min	Typ	Max	Unit
Output Voltage @ $I_{out} = 100\ \mu\text{A}$, $300\ \text{mA}$, $V_{in} = 3.2\ \text{V}$ and $T_A = 25^\circ\text{C}$ T_A from -25°C to $+85^\circ\text{C}$	V_{out}	2.73 2.70	2.77 2.77	2.81 2.84	V
Supply Current @ $I_{out} = 0$, $V_{in} = 3.2\ \text{V}$	I_{cc-0}	-	355	700	μA
Supply Current @ $I_{out} = 300\ \text{mA}$, $V_{in} = 3.2\ \text{V}$	I_{cc-300}	-	1.1	-	mA
Supply Current in Shutdown Mode (STDWN Pin Grounded) @ $T_A = 25^\circ\text{C}$	I_{stdwn}	-	0.01	1.0	μA
Dropout Voltage @ $I_{out} = 1.0\ \text{mA}$ (Note 4)	V_{drop-1}	-	0.6	-	mV
Dropout Voltage @ $I_{out} = 150\ \text{mA}$ (Note 4)	$V_{drop-150}$	-	75	150	mV
Dropout Voltage @ $I_{out} = 300\ \text{mA}$ (Note 4)	$V_{drop-300}$	-	150	250	mV
SET Threshold (SET = OUT) @ $I_{out} = 1.0\ \text{mA}$ and $V_{in} = 3.6\ \text{V}$ or $6.0\ \text{V}$, $T_A = 25^\circ\text{C}$ T_A from -25°C to $+85^\circ\text{C}$	V_{ref}	1.226 1.220	1.244 1.244	1.262 1.270	V
SET Input Leakage Current @ $V_{SET} = 1.25\ \text{V}$ and $T_A = 25^\circ\text{C}$	I_{leak}	-	10	200	nA
Short Circuit Output Current Limitation @ $V_{in} = 3.2\ \text{V}$ and $V_{out} = 2.2\ \text{V}$	I_{max_cc}	310	465	700	mA
Start-Up Current Limitation @ $T_A = 25^\circ\text{C}$, $V_{in} = 3.2\ \text{V}$ and $V_{out} = 2.2\ \text{V}$	I_{max_stup}	-	220	-	mA
Line Regulation, V_{in} varying between $3.0\ \text{V}$ and $6.0\ \text{V}$ @ $I_{out} = 1.0\ \text{mA}$	LineReg1	-0.1	0.01	0.1	%/V
Line Regulation, V_{in} varying between $3.0\ \text{V}$ and $6.0\ \text{V}$ @ $I_{out} = 10\ \text{mA}$	LineReg2	-0.1	0.01	0.1	%/V
Line Regulation, V_{in} varying between $3.0\ \text{V}$ and $6.0\ \text{V}$ @ $I_{out} = 1.0\ \text{mA}$ and (SET = OUT)	LineReg3	-0.1	0.03	0.1	%/V
Line Regulation, V_{in} varying between $3.0\ \text{V}$ and $6.0\ \text{V}$ @ $I_{out} = 10\ \text{mA}$ and (SET = OUT)	LineReg4	-0.1	0.03	0.1	%/V
Load Regulation, I_{out} varying from $0.1\ \text{mA}$ to $300\ \text{mA}$, SET = OUT, @ $V_{in} = 3.2\ \text{V}$	LoadReg1	-	0.0002	-	%/mA
Load Regulation, I_{out} varying from $0.1\ \text{mA}$ to $300\ \text{mA}$, SET Grounded, @ $V_{in} = 3.2\ \text{V}$	LoadReg2	-	0.001	-	%/mA
Output Voltage Noise @ SET = OUT, $C_{out} = 22\ \mu\text{F}$ (Note 5) $10\ \text{Hz} < f < 10\ \text{kHz}$ $10\ \text{Hz} < f < 100\ \text{kHz}$	-	-	15 35	-	μV_{rms}
Output Voltage Noise @ SET = GND, $C_{out} = 22\ \mu\text{F}$ (Note 5) $10\ \text{Hz} < f < 10\ \text{kHz}$ $10\ \text{Hz} < f < 100\ \text{kHz}$	-	-	35 60	-	μV_{rms}
Output Voltage Noise Density @ SET = GND, $C_{out} = 22\ \mu\text{F}$, $10\ \text{Hz} < f < 100\ \text{kHz}$ (Note 5)	-	-	400	-	$\text{nV}(\text{Hz})^{-1/2}$
Power Supply Rejection Ratio @ $1.0\ \text{kHz}$ and $I_{out} = 100\ \text{mA}$	PSRR	-	60	-	dB
Shutdown Threshold (with hysteresis) @ $V_{in} = 5.0\ \text{V}$	V_{stdwn}	0.63	-	2.65	V
Shutdown Pin Bias Current @ STDWN = IN or GND and $T_A = 25^\circ\text{C}$	I_{stdwn}	-	-	100	nA
FAULT Detection Voltage @ $I_{out} = 200\ \text{mA}$	$V_{fault-th}$	-	120	280	mV
FAULT Output Low Voltage @ $I_{sink} = 2.0\ \text{mA}$	$V_{fault-out}$	-	0.15	0.4	V
FAULT Output OFF Leakage Current @ $T_A = 25^\circ\text{C}$	I_{fault}	-	0.1	100	nA
Start-Up Time @ $C_{out} = 10\ \mu\text{F}$, $V_{out} = 2.7$, $I_{out} = 100\ \text{mA}$ (Note 5)	T_{stup}	-	60	-	μs
Thermal Shutdown Threshold	T_{limit}	-	170	-	$^\circ\text{C}$
Thermal Shutdown Hysteresis	H_{temp}	-	30	-	$^\circ\text{C}$

3. The specification gives the targeted values. This specification may have to be slightly adjusted after the temperature characterization of the die.

4. The dropout voltage is defined as ($V_{in} - V_{out}$) when V_{out} is 100 mV below the value of V_{out} when $V_{in} = 3.1\ \text{V}$.

5. Refer to characterization curves for more details.

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TYPICAL ELECTRICAL CHARACTERISTICS

($V_{in} = V_{out} + 0.5 \text{ V}$, $C_{in} = C_{out} = 2.2 \mu\text{F}$, SET = GND, $T_A = 25^\circ\text{C}$, unless otherwise noted.)

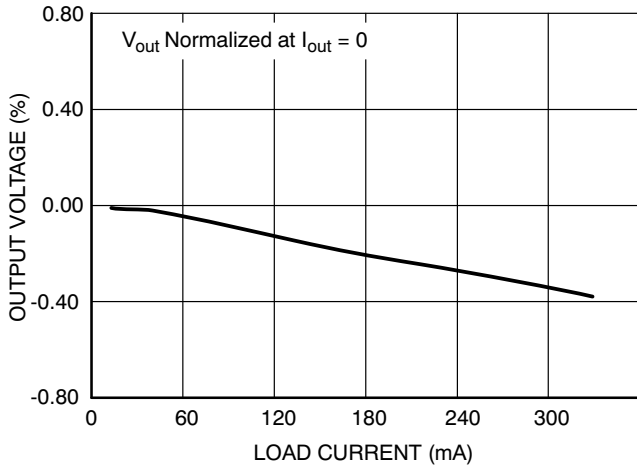


Figure 1. Normalized Output Voltage vs. Load Current

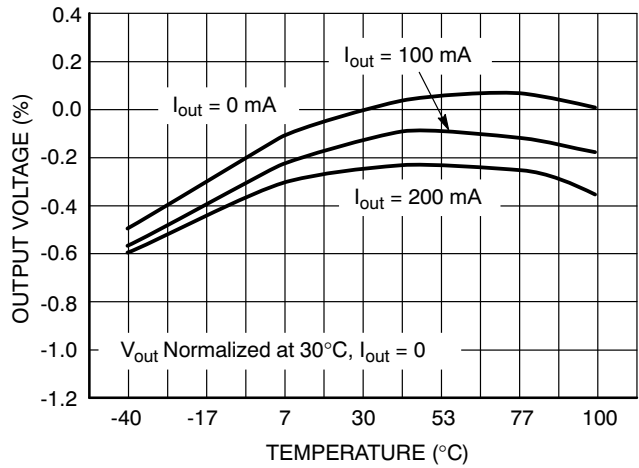


Figure 2. Normalized Output Voltage vs. Temperature

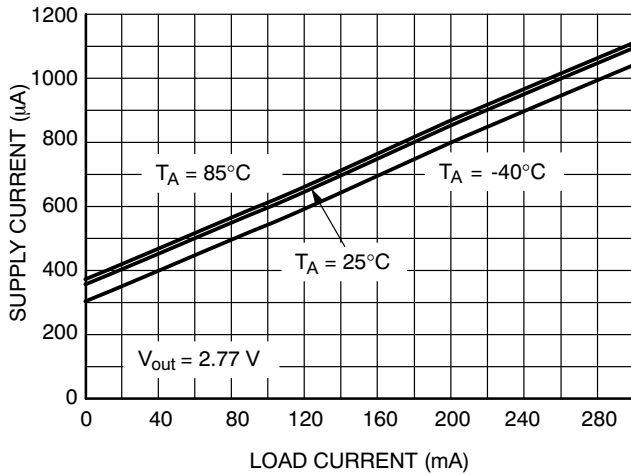


Figure 3. Supply Current vs. Load Current

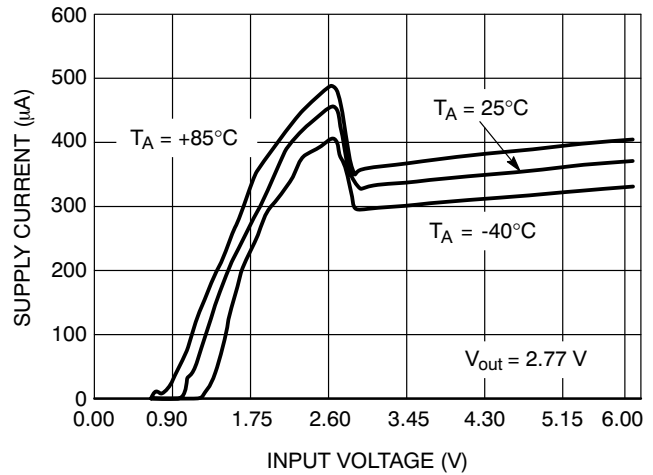


Figure 4. No Load Supply Current vs. Input Voltage

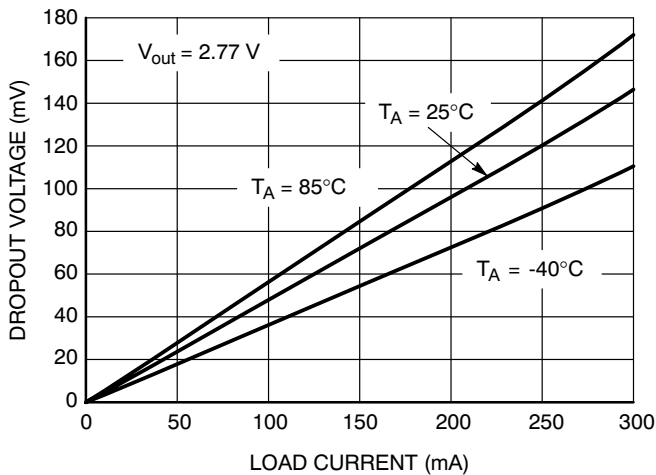


Figure 5. Dropout Voltage vs. Load Current

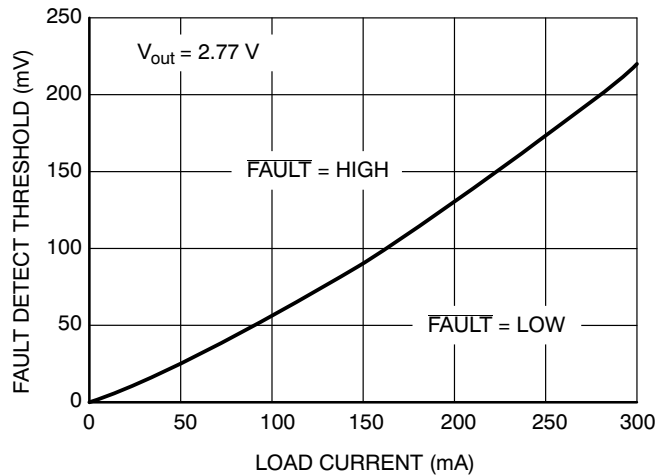


Figure 6. Fault Detect Threshold vs. Load Current

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TYPICAL ELECTRICAL CHARACTERISTICS

($V_{in} = V_{out} + 0.5\text{ V}$, $C_{in} = C_{out} = 2.2\ \mu\text{F}$, SET = GND, $T_A = 25^\circ\text{C}$, unless otherwise noted.)

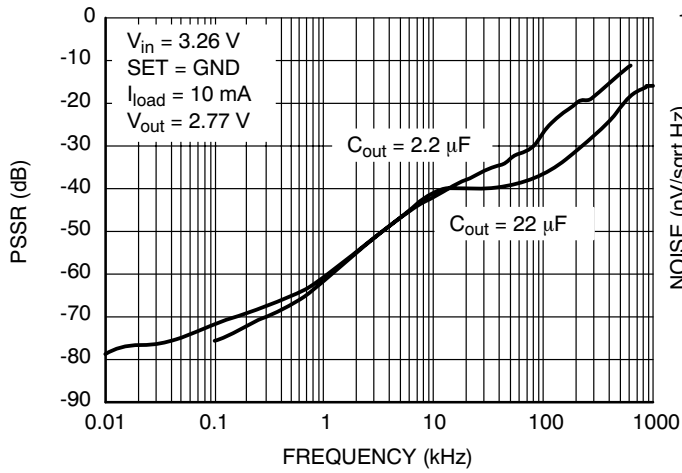


Figure 7. Power Supply Rejection Ratio

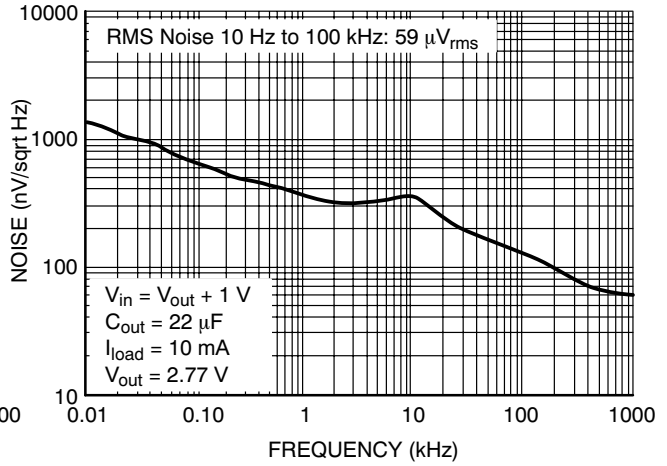


Figure 8. Output Noise Spectral Density

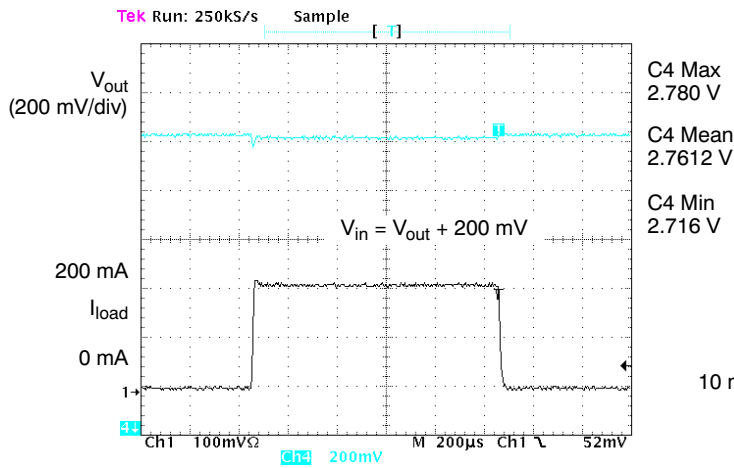


Figure 9. Load Transient Response

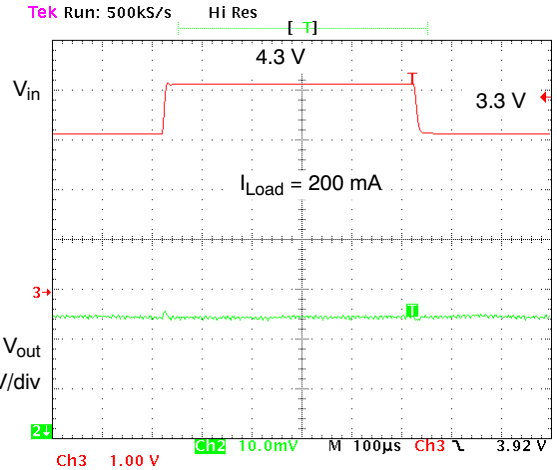


Figure 10. Line Transient

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TYPICAL ELECTRICAL CHARACTERISTICS

($V_{in} = V_{out} + 0.5\text{ V}$, $C_{in} = C_{out} = 2.2\ \mu\text{F}$, SET = GND, $T_A = 25^\circ\text{C}$, unless otherwise noted.)

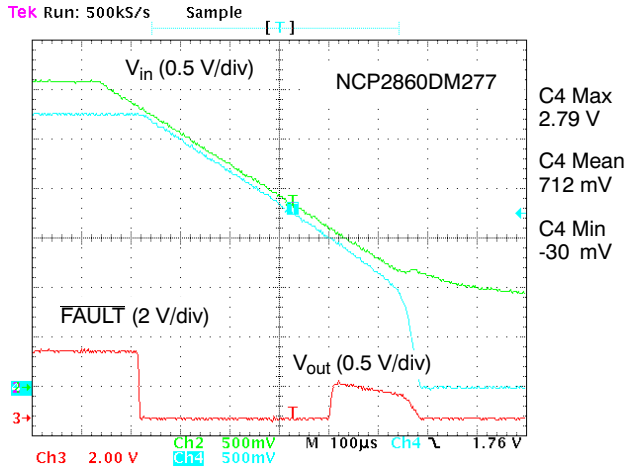


Figure 11. Power-Down Response ($I_{load} = 100\text{ mA}$)

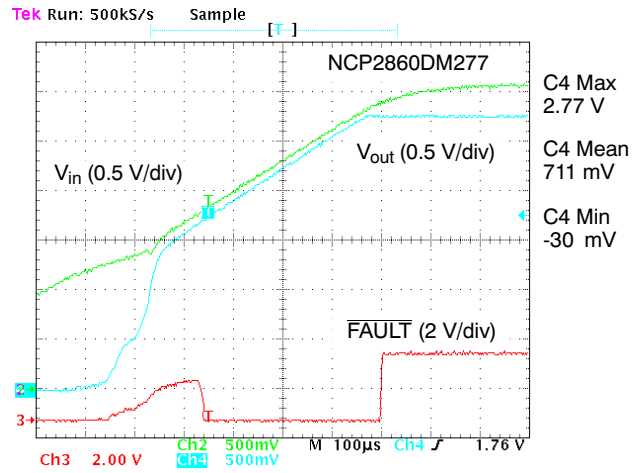


Figure 12. Power-Up Response ($I_{load} = 100\text{ mA}$)

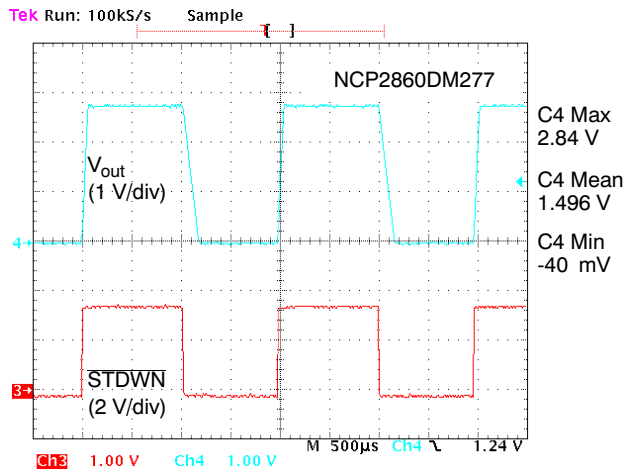


Figure 13. Shutdown/Power-Up
($V_{in} = V_{out} + 0.5\text{ V}$, $I_{load} = 50\text{ mA}$)

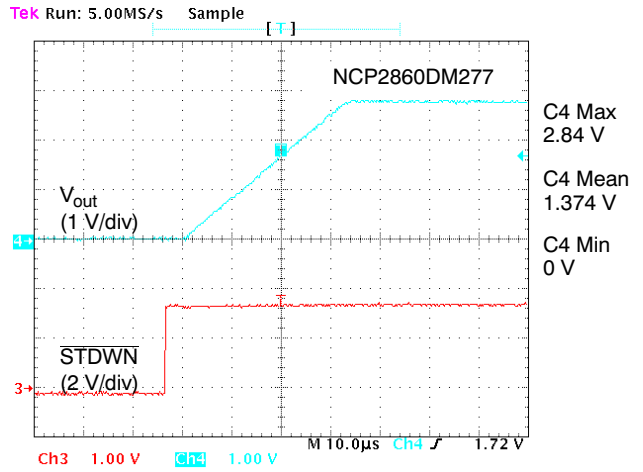


Figure 14. Shutdown/Power-Up
($V_{in} = V_{out} + 0.5\text{ V}$, $I_{load} = 0\text{ mA}$)

DETAILED OPERATING DESCRIPTION

Internal Pass Transistor

The NCP2860 incorporates a 0.5 Ω typical P-channel MOSFET pass transistor. The P-channel MOSFET requires no drive current and then compared to the PNP based regulators, this solution drastically reduces the quiescent current and associated losses.

Shutdown Block

The circuit turns into shutdown mode when the shutdown pin is in low state. In this mode, the internal biasing current sources are disconnected so that the pass transistor is off and the consumption reduced to a minimum value. Practically, the shutdown consumption is in the range of 10 nA. When this function is unused, “IN” is generally applied to the shutdown pin.

Current Limitation

The NCP2860 incorporates a short circuit protection that prevents the pass transistor current from exceeding 465 mA typically. The current limit is set to 220 mA during the start-up phase.

Thermal Protection

The thermal protection protects the die against excessive overheating. Practically, when the junction temperature exceeds 170°C for the 2.77 V option and 150°C for the 3.0 V option, an internal thermal sensor sends a logical signal to the shutdown block so that the circuit enters the shutdown mode. Once the die has cooled enough (typically 30°C), the circuit enters a new working phase.

Output Voltage Setting

The output voltage is set to 2.77 V if the “SET” pin is grounded. It can also be programmed to a different value. To do so, a portion of the output voltage must be applied to the “SET” pin. If a (R1, R2) resistors divider is used, then:

$$V_{out} = (1 + R1/R2) * V_{ref}$$

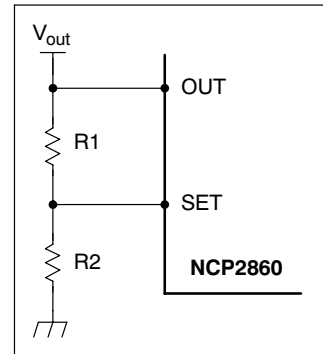
Therefore, as V_{ref} typically equals 1.244 V:

$$V_{out} = 1.244 * (1 + R1/R2).$$

Now if R1 and R2 are high impedance resistors, the leakage current that is absorbed by the “SET” pin, may have to be taken into account as follows:

$$V_{out} = [1.244 * (1 + R1/R2)] + (R1 * I_{lk}) \text{ where } I_{lk} \text{ is the "SET" pin leakage current.}$$

If the output voltage is directly applied to the “SET” pin, $V_{out} = V_{ref} = 1.244 \text{ V}$.



Regulation

The circuit incorporates a transconductance error amplifier. The error amplifier output varies in response to load and input voltage variations to control the pass transistor current so that the “OUT” pin delivers the wished voltage. No compensation capacitor is required.

Fault Detection Circuitry

The circuit detects when the input-output differential voltage is too low to ensure a correct load and line regulation at the output. The input-output differential threshold scales proportionally with the load current to be always just higher than the dropout.

When the circuit detects a fault condition, an internal switch connects “FAULT” to ground. In normal operation, the “FAULT” terminal is an open-drain-N-channel MOSFET and if a pull-up resistor is connected between “OUT” and “FAULT”, “FAULT” goes high. The pullup resistor is generally selected in the range of 100 kΩ to minimize the current consumption.

Application Information

It is recommended to use 2.2 μF capacitors on the input and on the output of the NCP2860. Capacitor type is not very critical. Simply the ESR should be lower than 0.5 Ω to ensure a stable operation over the temperature and output current ranges. It could be convenient to increase the capacitor size and its quality (lower ESR) only if it was necessary to further improve the noise performances, the Power Supply Rejection Ratio or the fast transient response.

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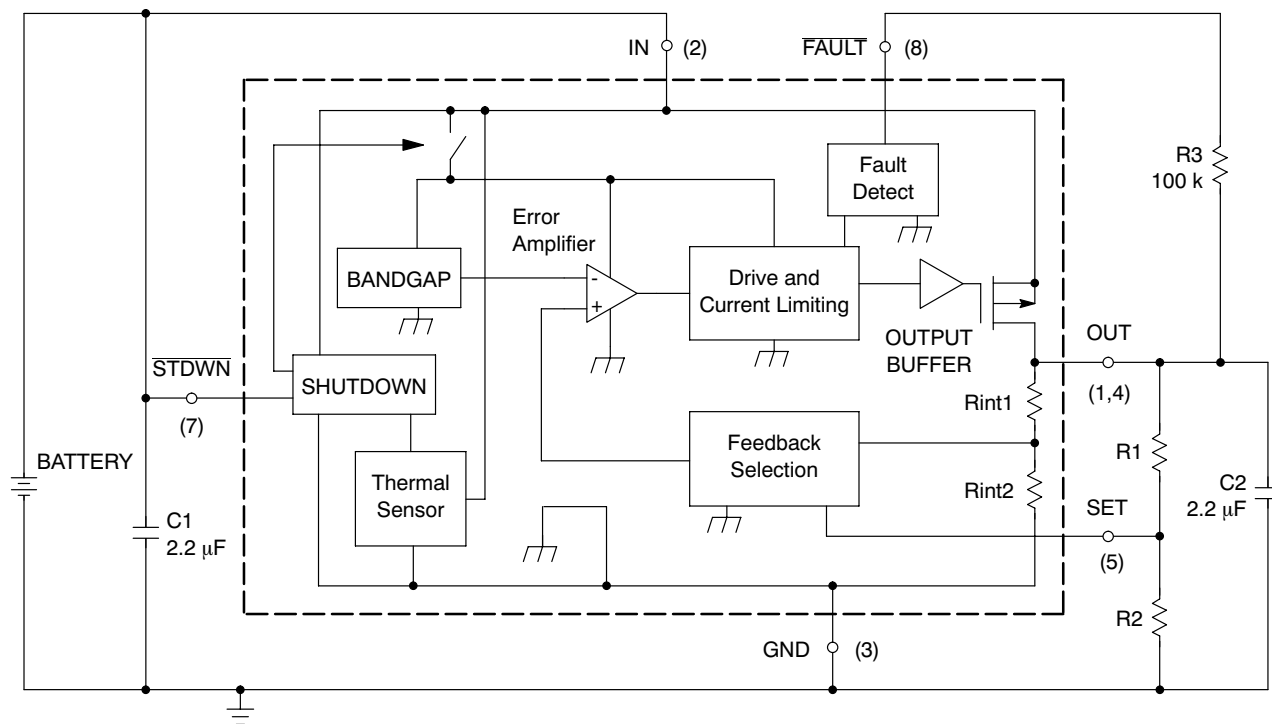


Figure 15. With External Output Voltage Adjustment

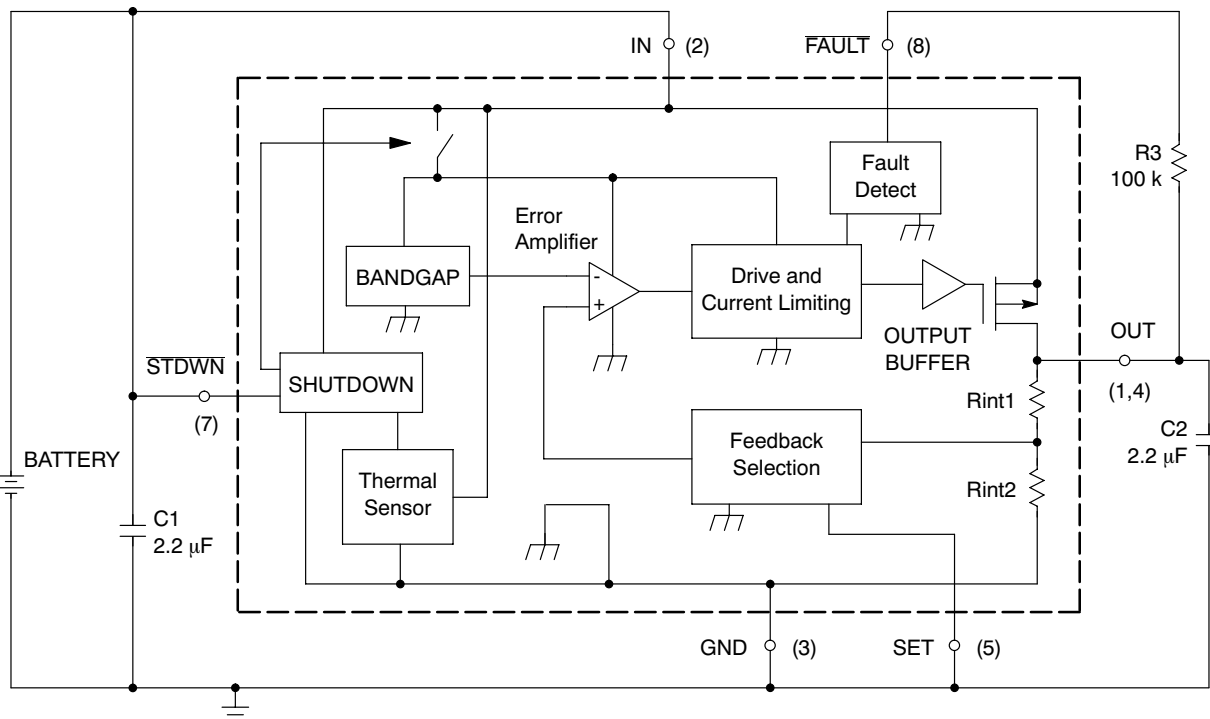
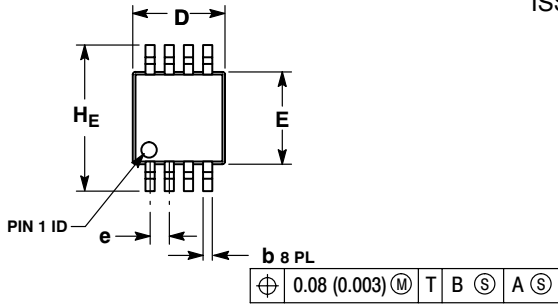


Figure 16. Application for 2.77 V Output Voltage

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PACKAGE DIMENSIONS

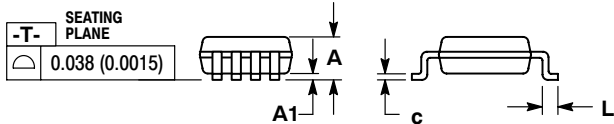
Micro8
CASE 846A-02
ISSUE G



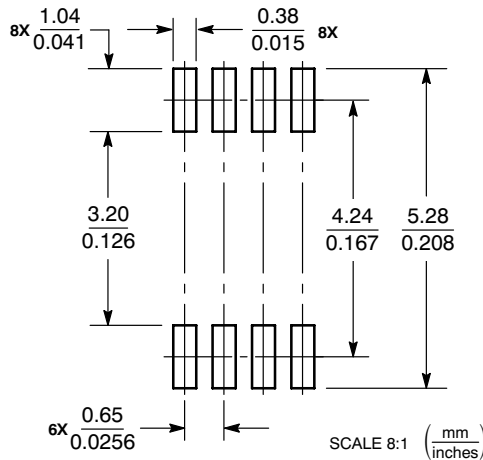
NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.
3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.
4. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE.
5. 846A-01 OBSOLETE, NEW STANDARD 846A-02.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	--	--	1.10	--	--	0.043
A1	0.05	0.08	0.15	0.002	0.003	0.006
b	0.25	0.33	0.40	0.010	0.013	0.016
c	0.13	0.18	0.23	0.005	0.007	0.009
D	2.90	3.00	3.10	0.114	0.118	0.122
E	2.90	3.00	3.10	0.114	0.118	0.122
e	0.65 BSC			0.026 BSC		
L	0.40	0.55	0.70	0.016	0.021	0.028
HE	4.75	4.90	5.05	0.187	0.193	0.199



SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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